

Figure 1.

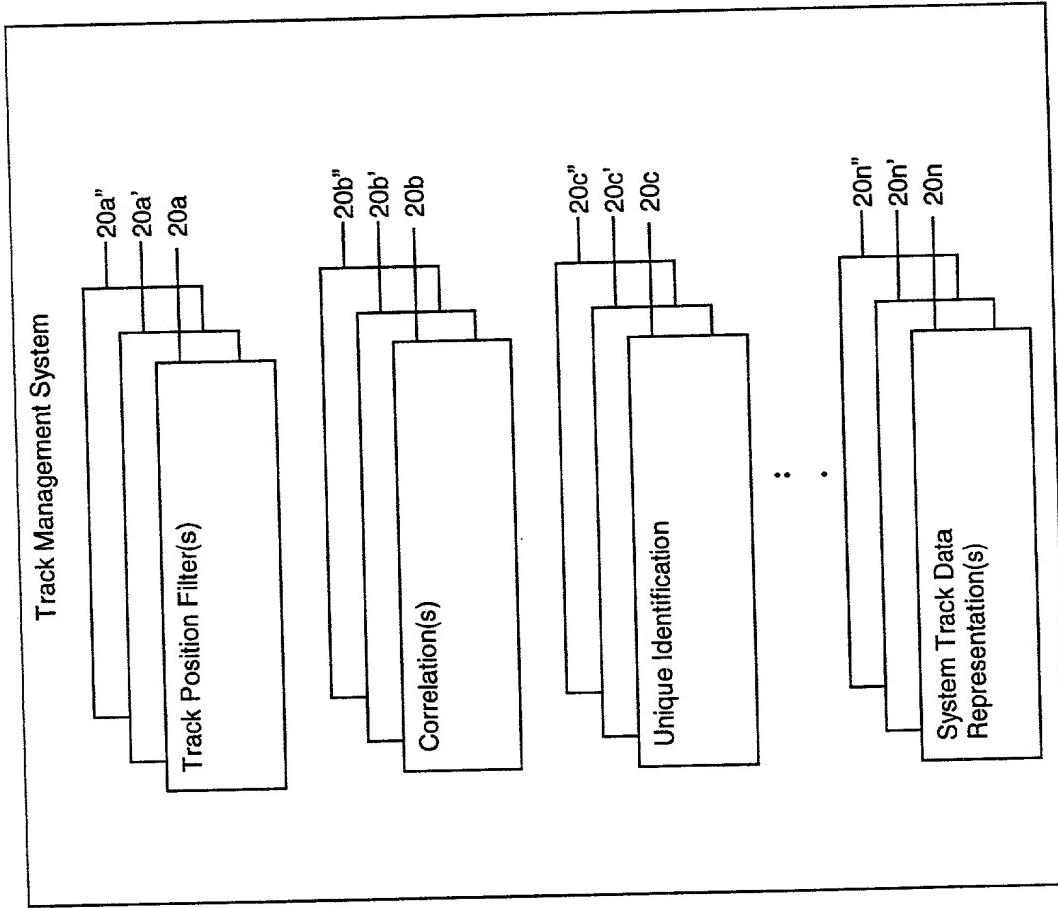


Figure 2.

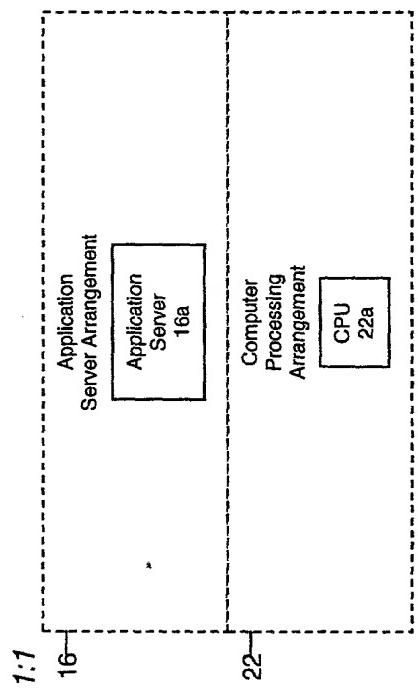


Figure 3a. One Application Server to One CPU

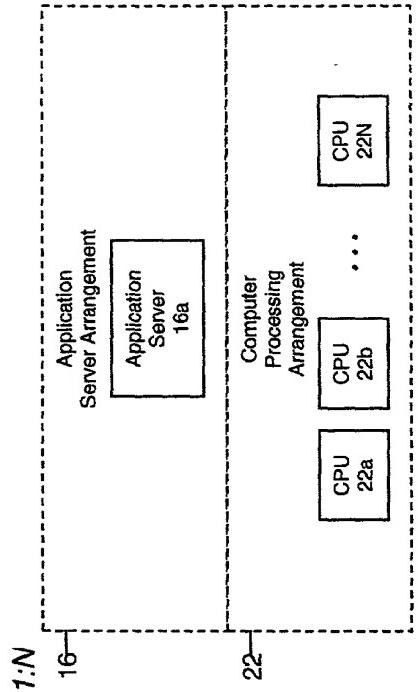


Figure 3b. One Application Server to Many CPUs

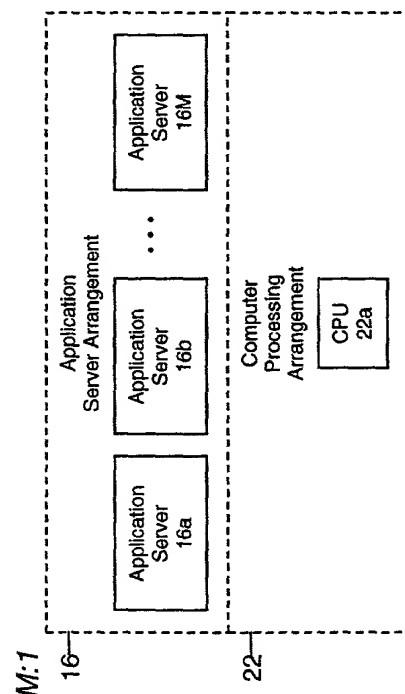


Figure 3c. Many Application Servers to One CPU

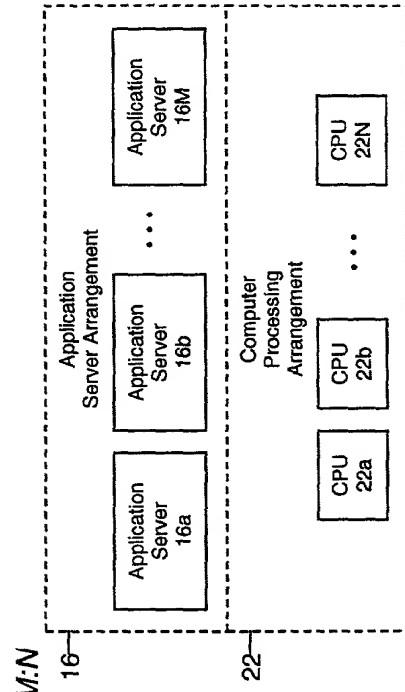


Figure 3d. Many Application Servers to Many CPUs

que la tasa de respuesta es menor que la tasa de respuesta de la configuración de software componentes.

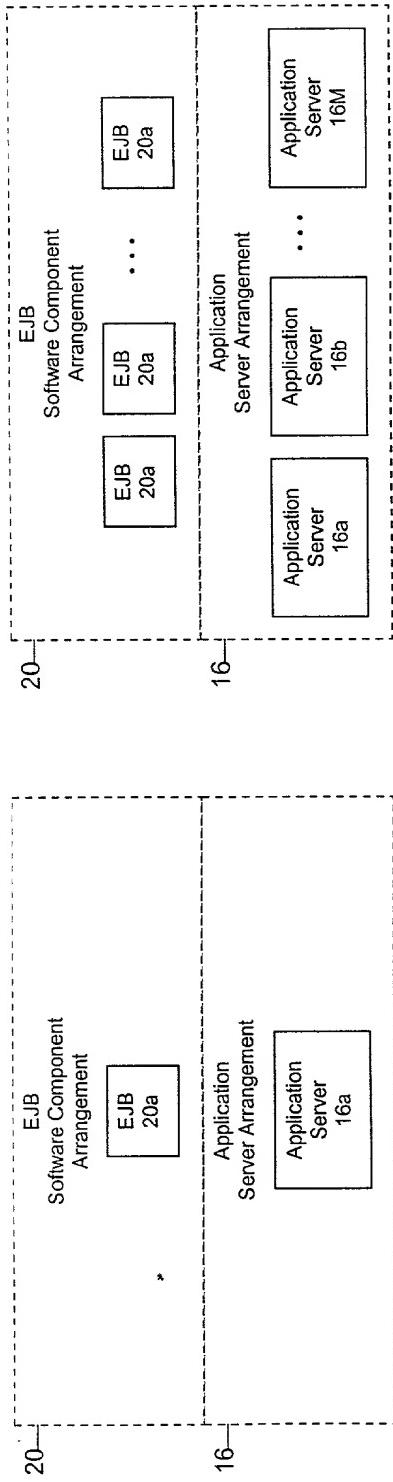


Figure 4a

Figure 4b

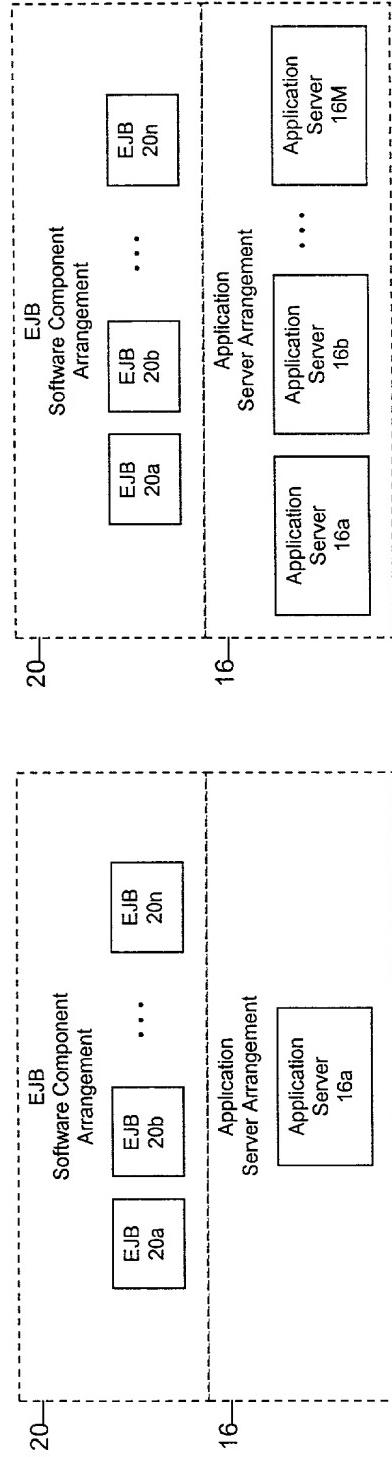


Figure 4c

Figure 4d

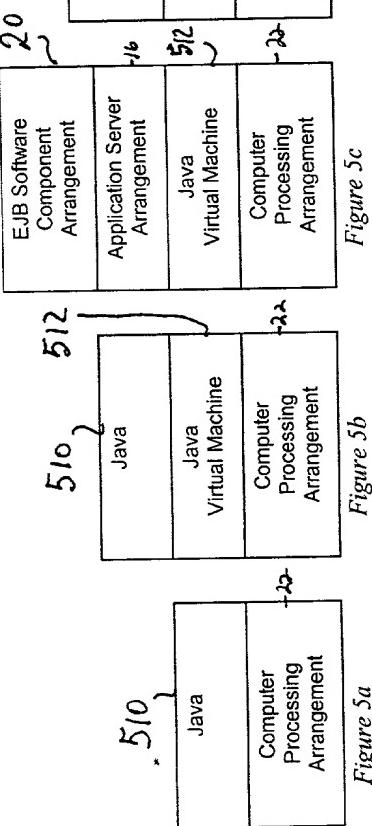


Figure 5a

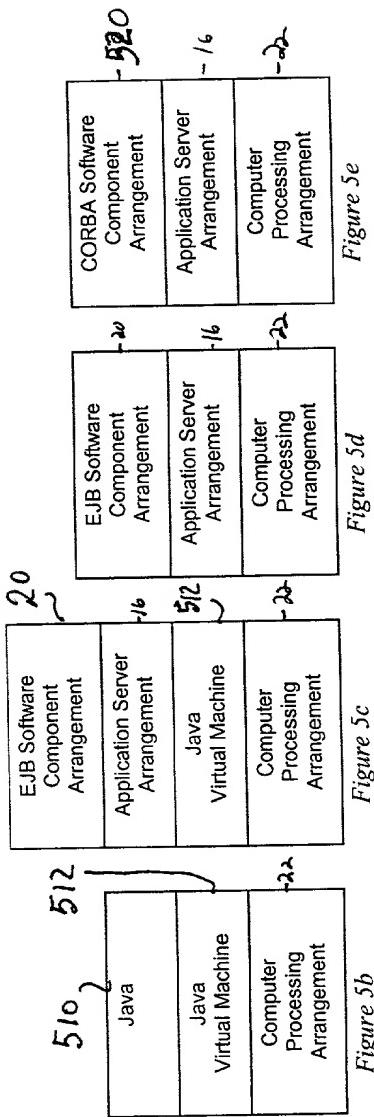


Figure 5b

Figure 5c

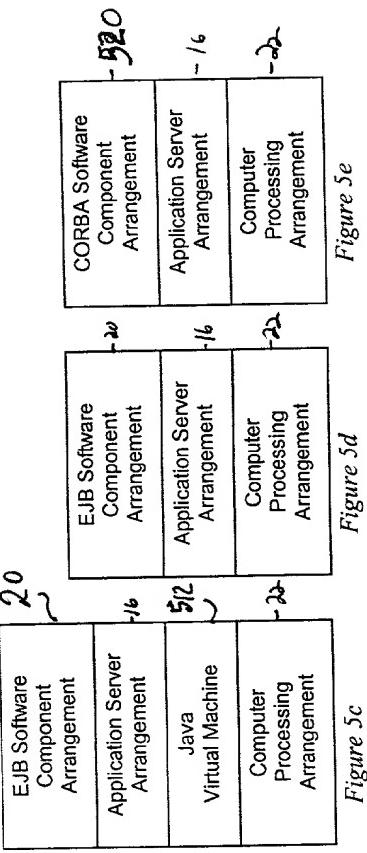


Figure 5c

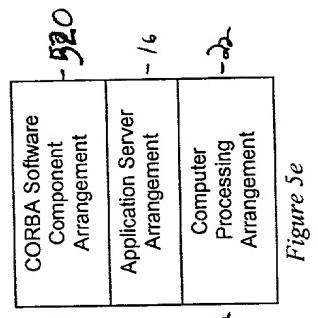


Figure 5d

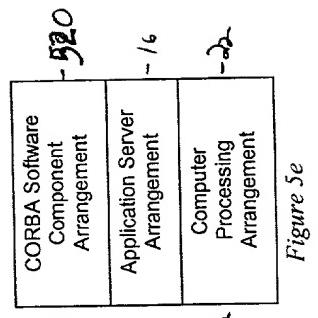


Figure 5e

GE-07053  
Underwood et al.  
Sheet 5 of 5